Dual receive, dual transmit fault tolerant network arrangement Patent Number: US5544151 Publication date: 1996-08-06 BAEK YOUNG S (KR); LEE KEUN W (KR); MIN BEYONG D (KR); CHOI GANG I (KR) Inventor(s): KOREA ELECTRONICS TELECOMM (KR) Applicant(s):: KR123064 Requested Patent: Application Number: US19950382539 19950202 Priority Number(s): KR19940032594 19941202 IPC Classification: H04L12/437 EC Classification: H04L12/437 Equivalents: JP2708389B2, JP8163164 **Abstract**

A fault permission network arrangement having a plurality of packet transfer units connected in a ring form. Each of the plurality of packet transfer units comprises first and second nodes, each of the first and second nodes having two inputs and two outputs, for inputting the same data from previous-stage two nodes, abandoning one of the input data and transferring the other data to subsequent-stage two nodes, and an interface unit having two inputs and two outputs connected to the first and second nodes through serial links, respectively, for transferring a packet from a communication system to the first and second nodes, the interface unit selecting one of the same two data from the first and second nodes and transferring the selected data to the communication system. According to the present invention, the selected packet can be transferred to a destination although faults occur at a plurality of nodes, respectively.

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